

## Electronic Acknowledgement Receipt

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| <b>Application Number:</b>                  | 10517258   |
| <b>International Application Number:</b>    |  |
| <b>Confirmation Number:</b>                 | 1207   |
| <b>Title of Invention:</b>                  | Semiconductor intergrated circuit device, data storage verification device, and data storage verification method |
| <b>First Named Inventor/Applicant Name:</b> | Katsuhiro Nakai  |
| <b>Customer Number:</b>                     | 513  |
| <b>Filer:</b>                               | Jeffrey Richard Filipek/Kara Reade   |
| <b>Filer Authorized By:</b>                 | Jeffrey Richard Filipek  |
| <b>Attorney Docket Number:</b>              | 2004_1946A   |
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| <b>Application Type:</b>                    | U.S. National Stage under 35 USC 371   |

### Payment information:

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### File Listing:

| Document Number | Document Description                     | File Name   | File Size(Bytes) /Message Digest                  | Multi Part /.zip | Pages (if appl.) |
|-----------------|--|-------------|---|------------------|------------------|
| 1               | Response to Election / Restriction Filed | AttachA.pdf | 92546<br>e784e83885e07c56db812a77bbf252a1846712e3 | no               | 1                |

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**If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.**

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**If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.**

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